11-2. Image Erase Lamp LA2 ON/OFF Control

11-2-1. ON/OFF Control Data Output

Master Board PWB-A outputs 32-bit ON pattern data (DATA) to Image Erase Lamp LA2 to turn ON and OFF particular LEDs of LA2.
The ON pattern data is divided into four blocks of data (each 8 bits) output every 5 msec.

Signals sent from PWB-A are as follows:

DATA:	Indicates which LED to turn ON or OFF of LA2.
CLK:	Synchronizing signal to transmit data serially.
MODE:	Latches the LED ON/OFF data sent from PWB-A.
STROBE:	Outputs the latched LED ON/OFF data to turn ON the corresponding LEDs.

11-2-2. Leading Edge Erase

All LA2 LEDs are turned ON and kept ON for a given period time after SCP Board PWB-J has generated an Image Leading Edge signal (BASE) to erase the image along the leadingedge.

The erase time varies depending on whether SCH-20 (Leading Edge Erase) of the Tech. Rep. Choice is set to "Erase" or "Not Erase" and on the control panel setting. See Table below.

Function	SCH-20 "Erase" (4 mm)				"Not Erase"				Frame Erase	Punch Hole Erase		
	Normal	File Margin			Normal	File Margin						
Erase Width Setting		10	15	20		10	15	20	10	10	15	20
Image Erase Width	4	14	19	24	0	10	15	20	10	10	15	20
Timer	140	205	240	275	115	180	215	250	180	180	215	250

Unit: Erase width = mm, Timer = msec.